| FACULTY: | Faculty of Electronics and Computer Science |
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| FIELD OF STUDY: | Electronics and Telecommunications |
| ERASMUS COORDINATOR OF THE FACULTY: | Marcin Walczak, PhD (Electronics)  Robert Wirski, PhD (Computer Science) |
| E-MAIL ADDRESS OF THE COORDINATOR: | marcin.walczak@tu.koszalin.pl  robert.wirski@tu.koszalin.pl |
| COURSE TITLE: | Computer Architecture and Operating Systems |
| LECTURER’S NAME: | Dariusz Gretkowski, PhD |
| E-MAIL ADDRESS OF THE LECTURER: | dariusz.gretkowski@tu.koszalin.pl |
| ECTS POINTS FOR THE COURSE: | 3 |
| COURSE CODE (USOS): | 0711>0400-AKiSO |
| ACADEMIC YEAR: | 2022/2023 |
| SEMESTER:  (W – winter, S – summer) | S |
| HOURS IN SEMESTER: | 30 |
| LEVEL OF THE COURSE:  (1st cycle, 2nd cycle, 3rd cycle) | 1st cycle |
| TEACHING METHOD:  (lecture, laboratory, group tutorials, seminar, other-what type?) | Lecture – 30h |
| LANGUAGE OF INSTRUCTION: | English\* |
| ASSESSMENT METHOD:  (written exam, oral exam, class test, written reports, project work, presentation, continuous assessment, other – what type?) | Written exam or class test |
| COURSE CONTENT: | Architecture of the computer system. Types of memory and types of addressing in computer systems. Pipelined and scalar instruction processing. Structure and functioning of the cooprocessor. Structure and functioning of MMX, SSE, and AVX blocks. Functional - structural organization of the computer system. Architectures of arithmetic and logic systems. Systems controlling the operation of a computer system. Microprogram control blocks. Structures of various microprogram control blocks with absolute and relative addressing. Architex program for designing and simulating a computer system. The concept of the operating system (structure, main tasks, and common issues). |
| ADDITIONAL INFORMATION: |  |

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/sporządził, data/

\*kurs dostępny wyłącznie w języku angielskim